



Attorney's Docket No. 10559-627001 / P11061

2182

#3 IDS
5/21/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andrew J. Fish et al. Art Unit : 2182
Serial No. : 10/016,055 Examiner : Unknown
Filed : December 11, 2001
Title : SUPPORTING LEGACY OPERATING SYSTEM BOOTING IN A
LEGACY-FREE SYSTEM

Commissioner for Patents
Washington, D.C. 20231

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Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information
Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first
Office action on the merits. No fee is required.

The documents are in the English language; hence no concise
explanation is necessary per Rule 98(a)(3).

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Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Respectfully submitted,

Date: 3/8/02

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Substitute Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-627001	Application No. 10/016,055
Information Disclosure Statement by Applicant (Use several sheets if necessary)		
Applicant Andrew J. Fish et al.		Filing Date December 11, 2001
Group Art Unit 2182		

U.S. Patent Documents

Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	6,081,890	June 27, 2000	Datta			November 30, 1998
	AB						
	AC						
	AD						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AE							
	AF							
	AG							
	AH							
	AI							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
✓	AJ	"BIOS Basics," BIOS CENTRAL, http://www.bioscentral.com/misc/biosbasics.htm , March 4, 2002
✓	AK	Mark Doran - Intel Corporation, "Extensible Firmware Interface - Changing the face of BIOS," August 28-30, 2001, Intel Developer Forum, Fall 2001
	AL	Sverre Jarp, "IA-64 architecture - A Detailed Tutorial," Version 3 http://nicewww.cern.ch/~sverre/SJ.html , November 8, 2001
	AM	Sunil Saxena, "IA-64 Architecture," Intel Corporation, September 11, 2000
	AN	Intel, "Extensible Firmware Interface Specification," Version 1.02, December 12, 2000

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	